

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	L1	28646 1	integrated adj3 circuit	USPAT ; EPO; JPO	2004/07/14 13:56	
2	BRS	L2	79	1 and ((LNA or (low adj2 noise adj2 amplifier)) same (variable adj2 amplifier))	USPAT ; EPO; JPO	2004/07/14 13:57	
3	BRS	L3	61	2 and (gain adj2 control\$4)	USPAT ; EPO; JPO	2004/07/14 13:58	
4	BRS	L4	16	("4282515" "4488144" "4495470" "5087914" "5153592" "5397944" "6049246" "6114980" "6166668" "6225848" "6317064" "6356217" "6356218" "6360087" "6459889" "6642868") .PN.	USPAT	2004/07/14 14:00	

	Error Definition	Er ro rs
1		0
2		0
3		0
4		0